

Abstracts

Monolithic Ka-Band FET Receiver Chips (1994 Vol. I [MWSYM])

R. Reeves and V. Brady. "Monolithic Ka-Band FET Receiver Chips (1994 Vol. I [MWSYM])." 1994 MTT-S International Microwave Symposium Digest 94.1 (1994 Vol. I [MWSYM]): 119-121.

Integrated SSB low noise receivers have been developed using 0.25 um PHEMT mushroom gate process. The chip includes low noise amplifiers, miniature image noise filter, and FET downconverters to achieve less than a 4 dB SSB NF. These circuits can be used in SSB communication systems or in monopulse radar receiver applications where they will reduce channel to channel tracking errors, test, and assembly costs compared to a three chip assembly of LNA, filter, and downconverter.

 [Return to main document.](#)